

A

A

B

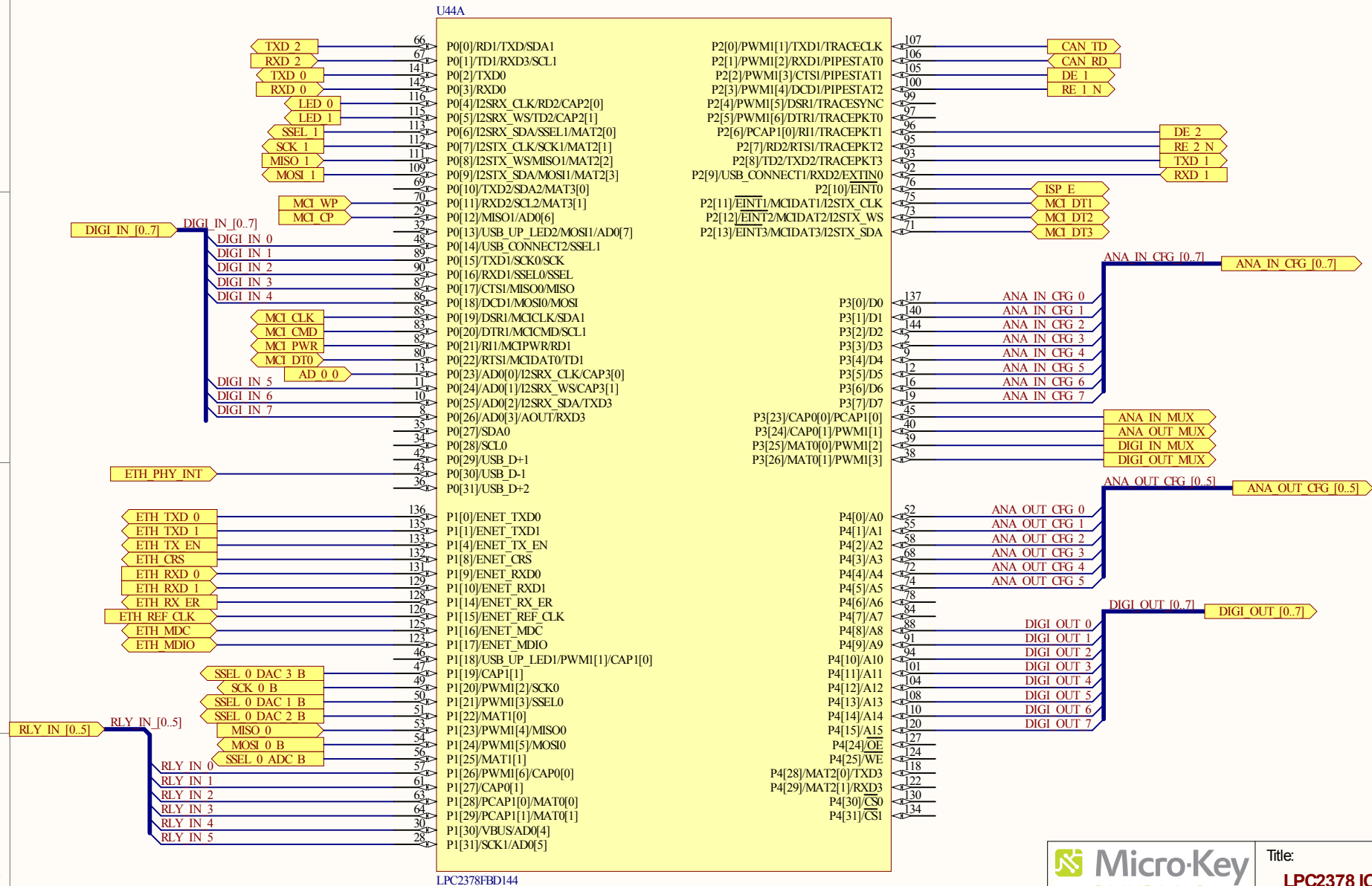
B

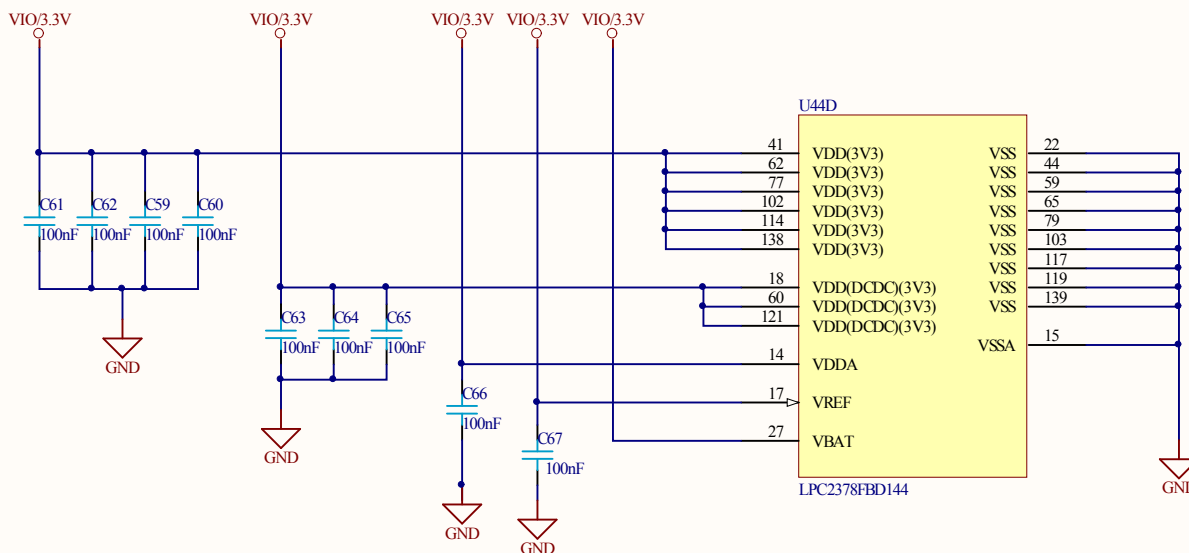
C


C

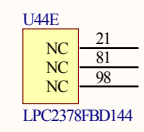
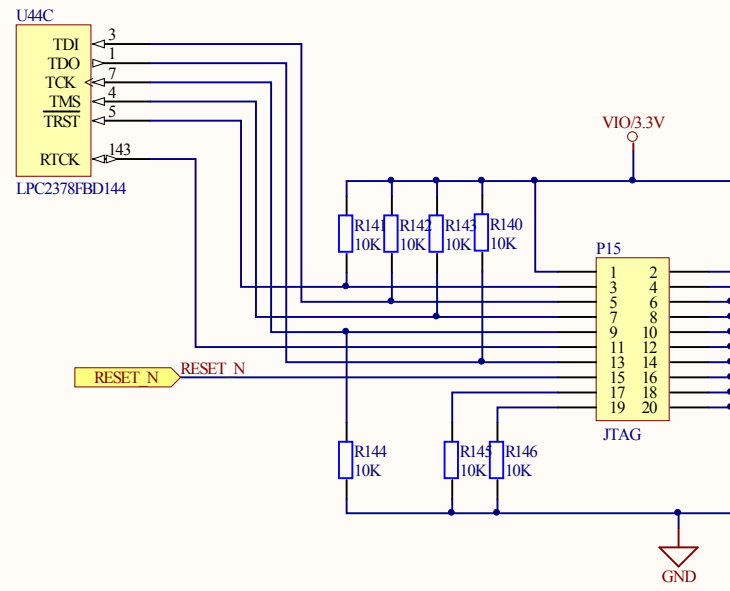
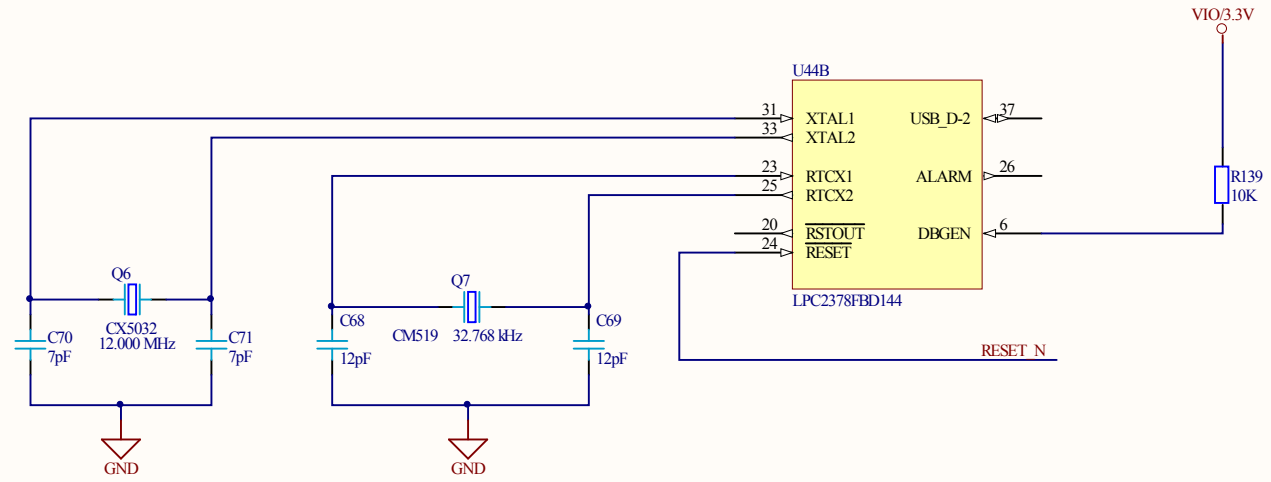
D


D





		Title: LPC2378 Powersupply	
Drawn: MM	Number: 04.	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 04 of: 49	
File: P:\QUA_2475\Tester\HMSCHLPC2378 Power.SchDoc			



 Micro-Key Embedded Technology Experts		Title: LPC2378 SYS	
Drawn: MM	Number: 06	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 06 of: 49	
File: P:\QUA_2475\Tester\HWSCHLPC2378_SYS.SchDoc			

A

B

C

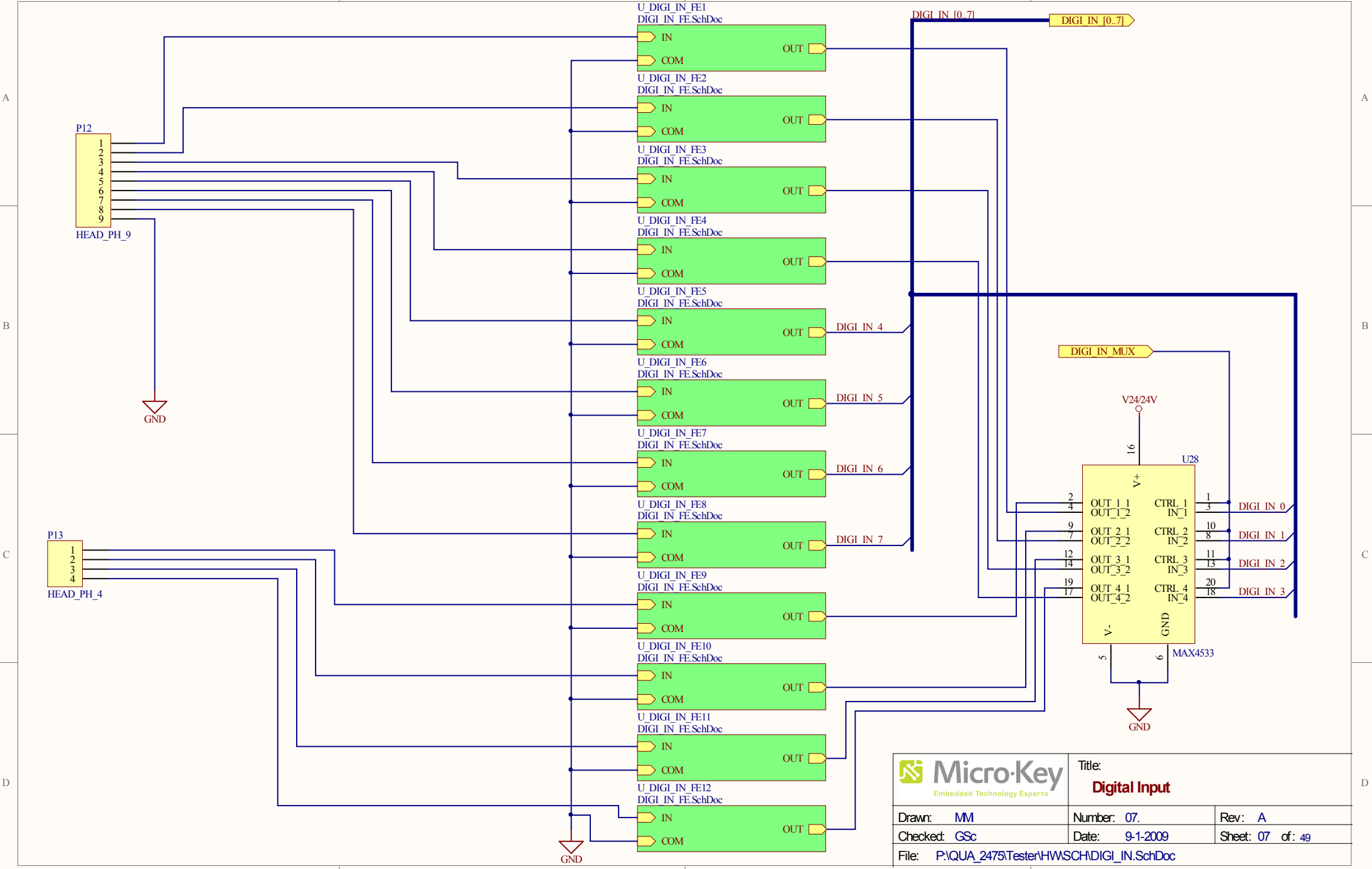
D

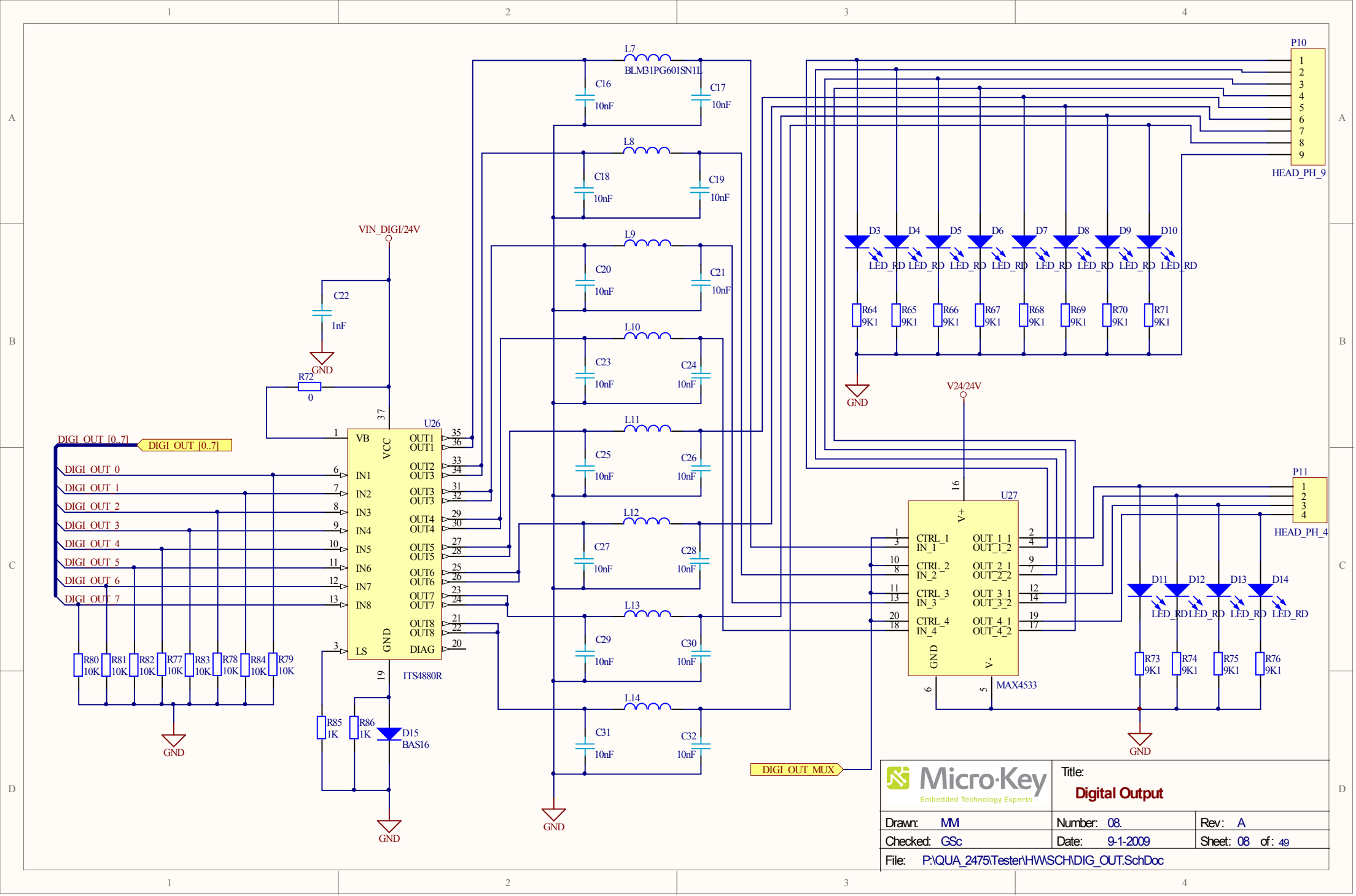
A

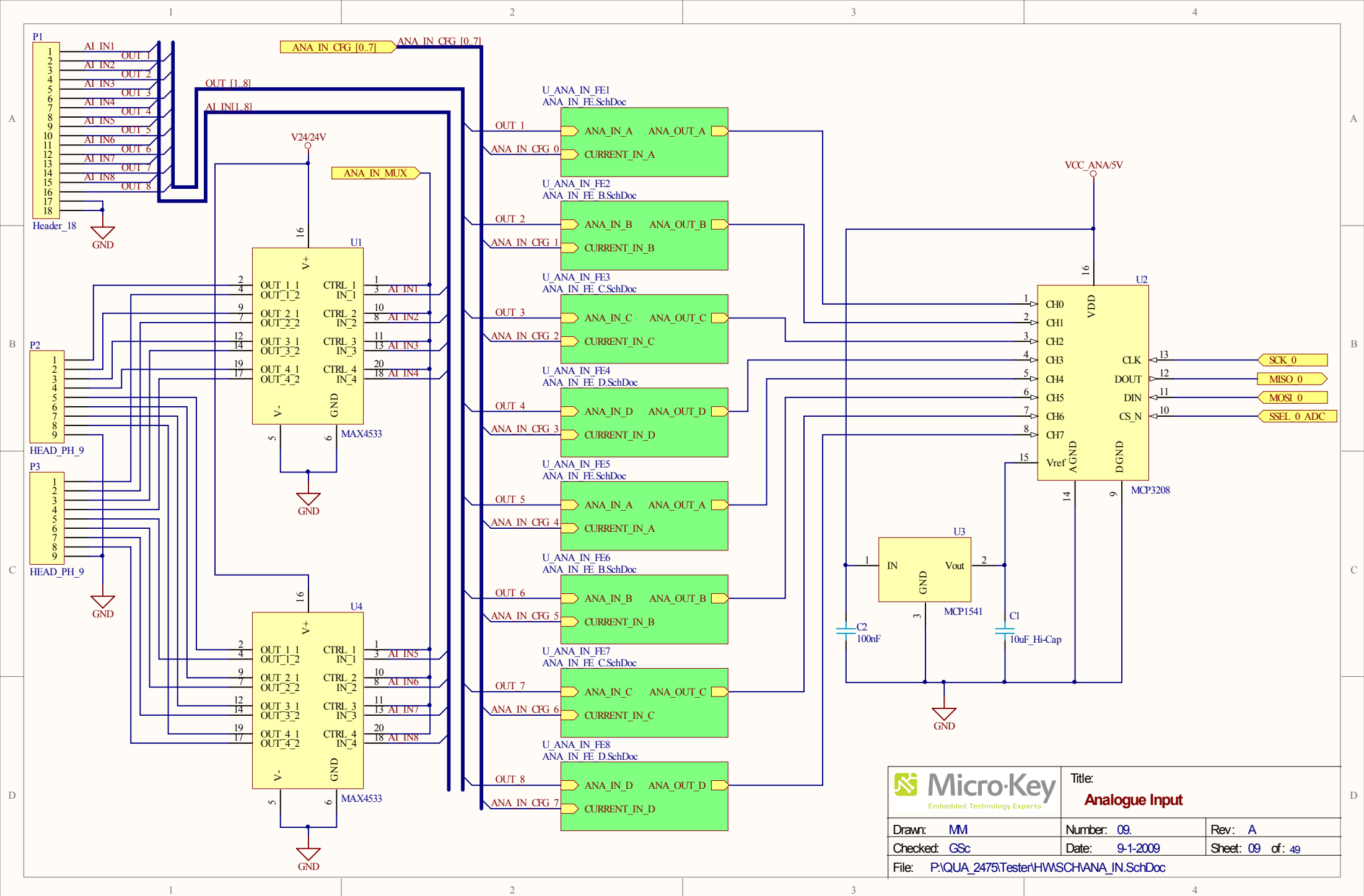
B

C

D







A

B

C

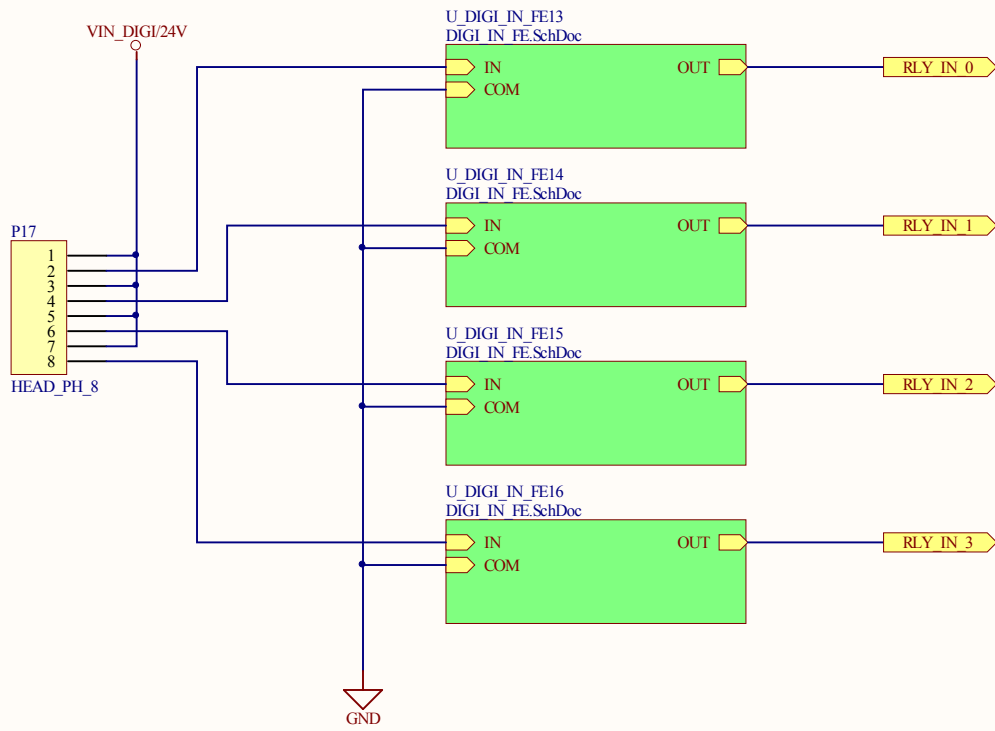
D


A

B

C

D



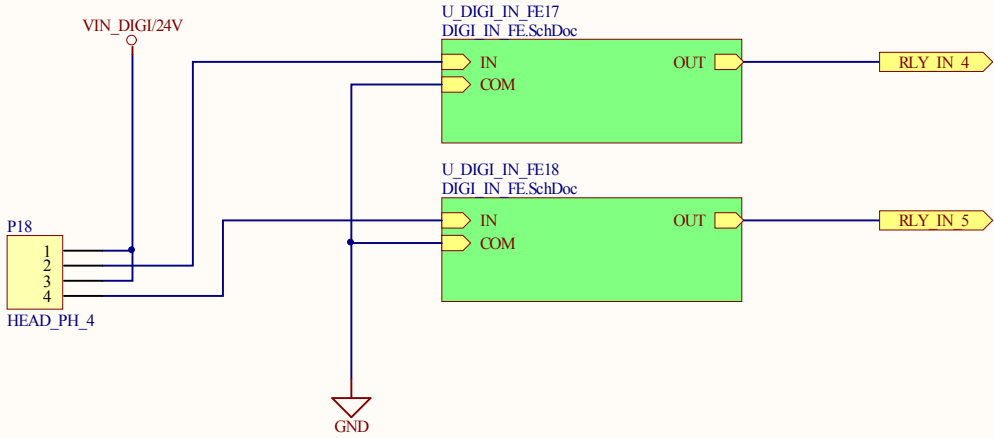
 Micro-Key Embedded Technology Experts		Title: Relay Input	
Drawn: MM	Number: 011.	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 11 of: 49	
File: P:\QUA_2475\Tester\HMSCHIRLY_IN.SchDoc			


A

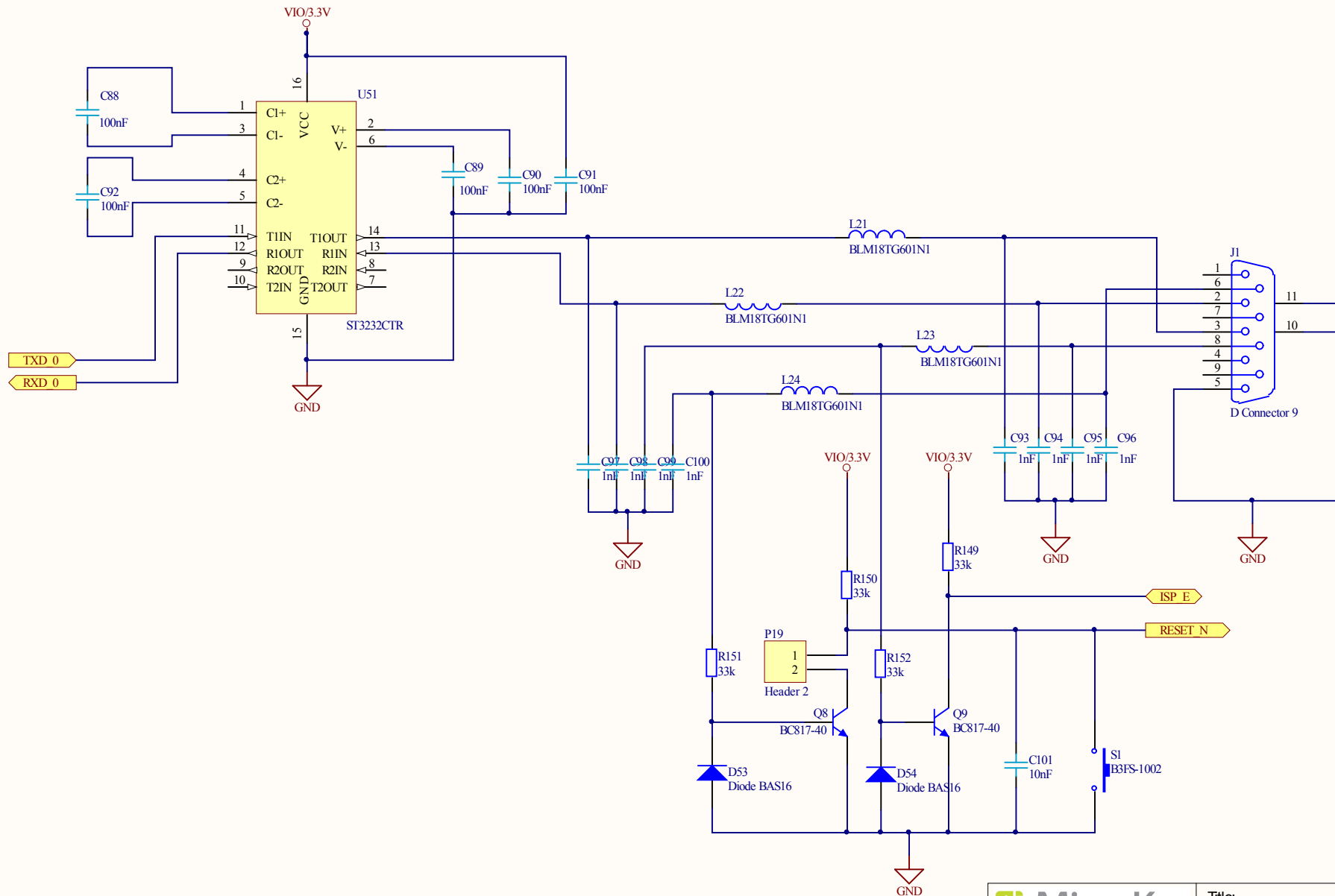
B


C

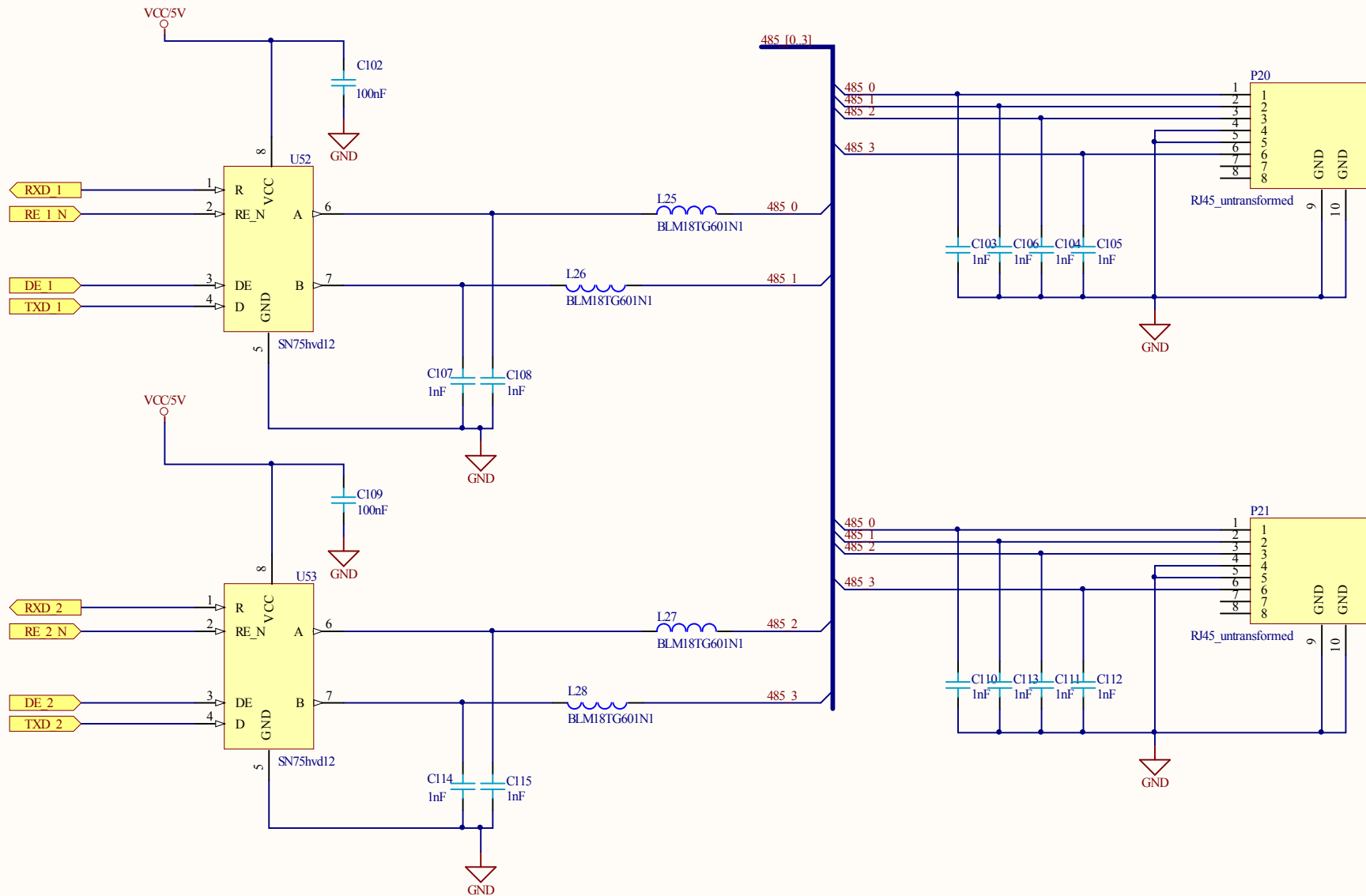
D




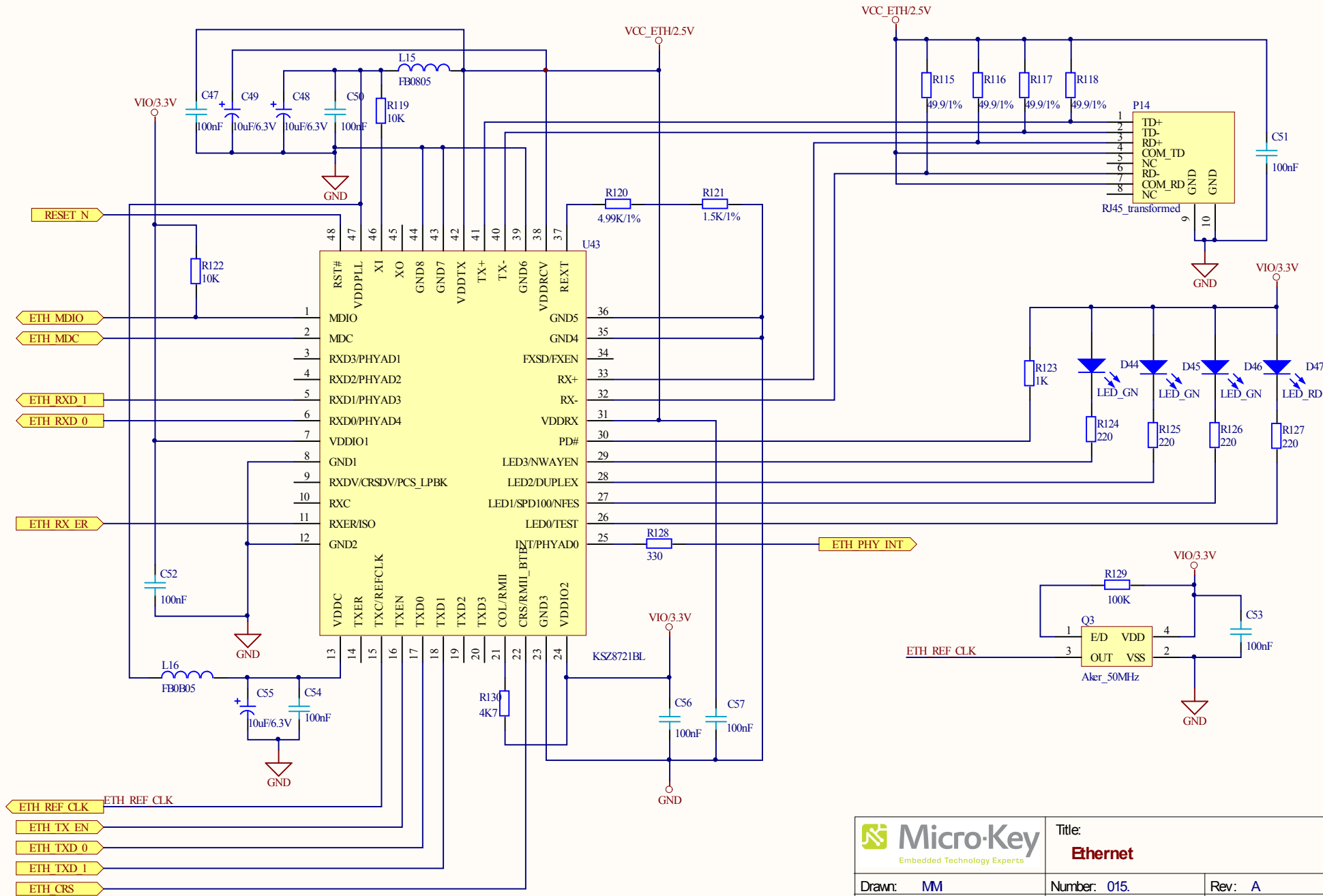
 Micro-Key <small>Embedded Technology Experts</small>	Title: Relay Input EB	
	Drawn: MM	Rev: A
	Checked: GSc	Sheet: 12 of: 49
	File: P:\QUA_2475\Tester\HWSCHARLY_IN_EB.SchDoc	

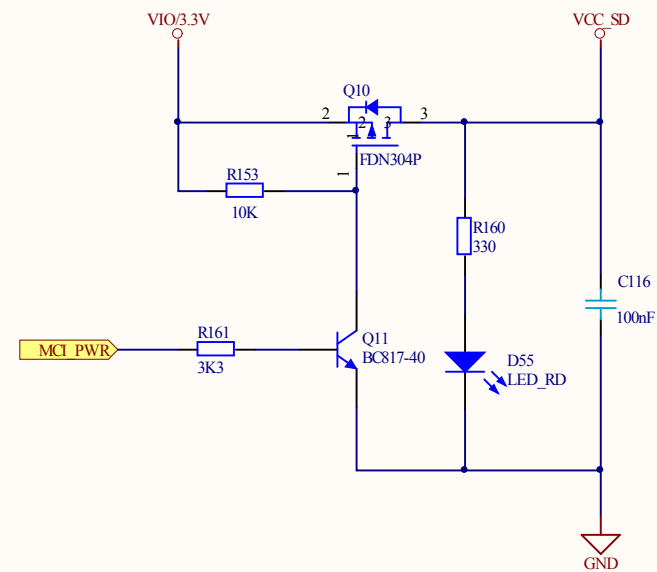
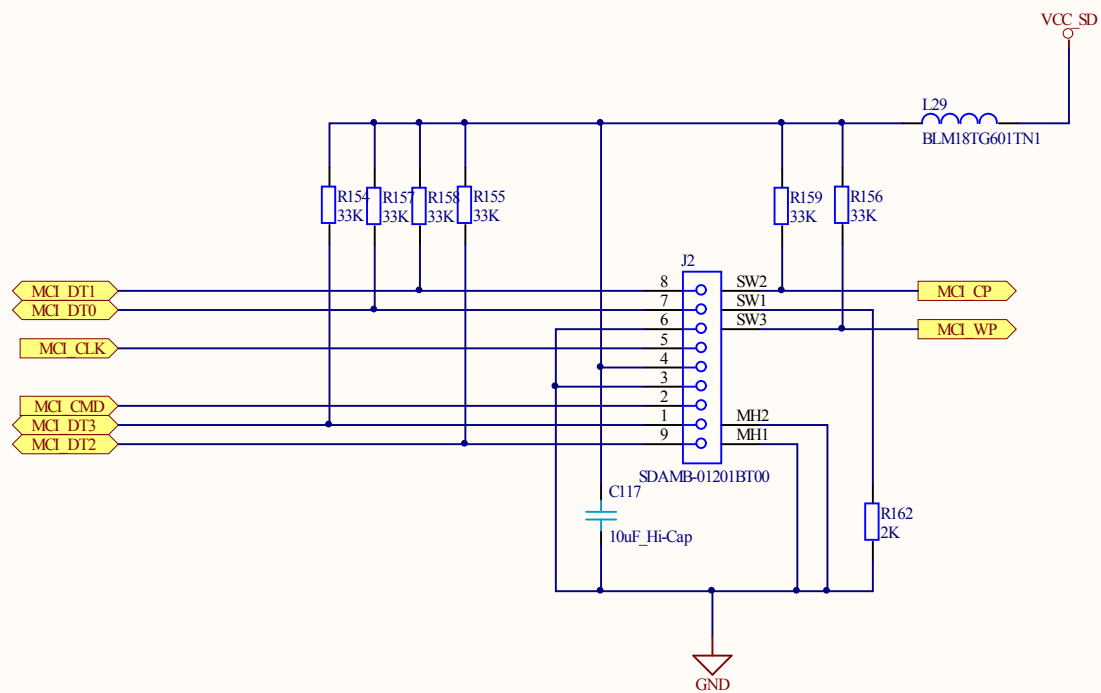



 Micro-Key Embedded Technology Experts		Title: RS 232	
Drawn: MM	Number: 013.	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 13 of: 49	
File: P:\QUA_2475\Tester\HWSCH\RS232.SchDoc			

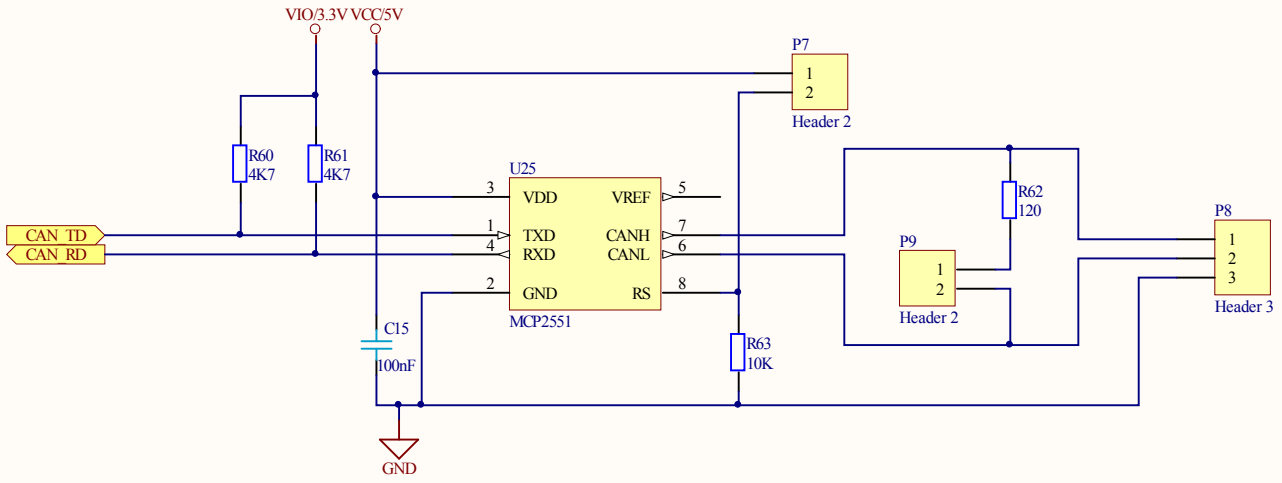



 Micro-Key Embedded Technology Experts		Title: RS 485	
Drawn: MM	Number: 014.	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 14 of: 49	
File: P:\QUA_2475\Tester\HWSCHRS485.SchDoc			

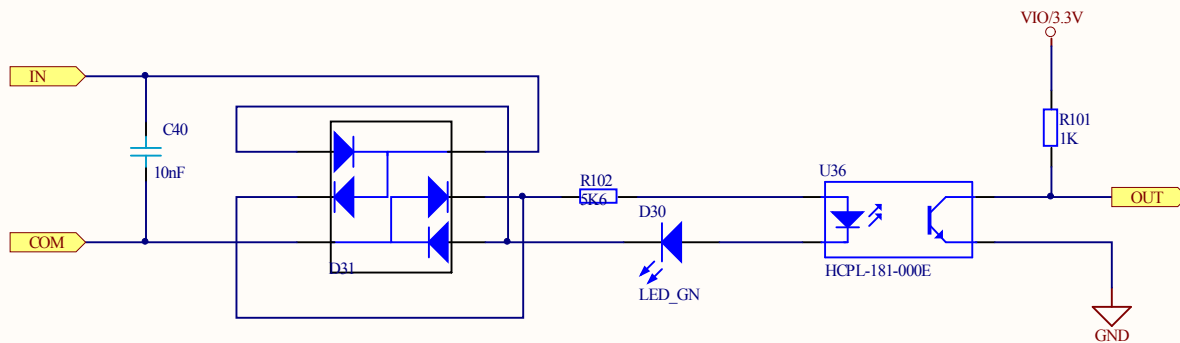





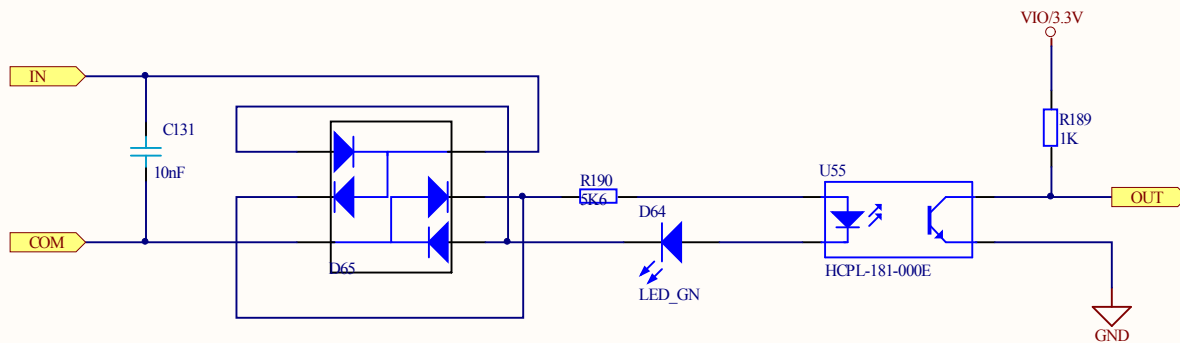
 Micro-Key Embedded Technology Experts		Title: SD-Card	
Drawn: MM	Number: 016.	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 16 of: 49	
File: P:\QUA_2475\Tester\HWSCHSD_CARD.SchDoc			




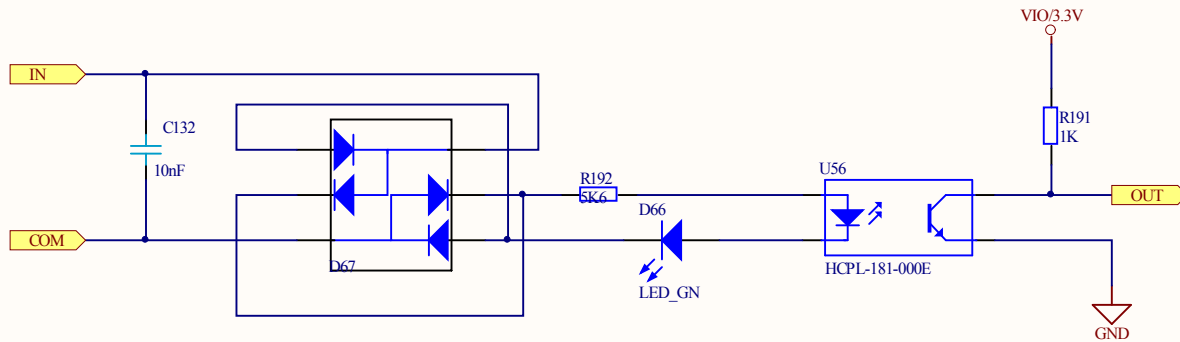
 Embedded Technology Experts	Title: CAN	
Drawn: MM	Number: 017.	Rev: A
Checked: GSc	Date: 9-1-2009	Sheet: 17 of: 49
File: P:\QUA_2475\Tester\HMSCH\CAN.SchDoc		




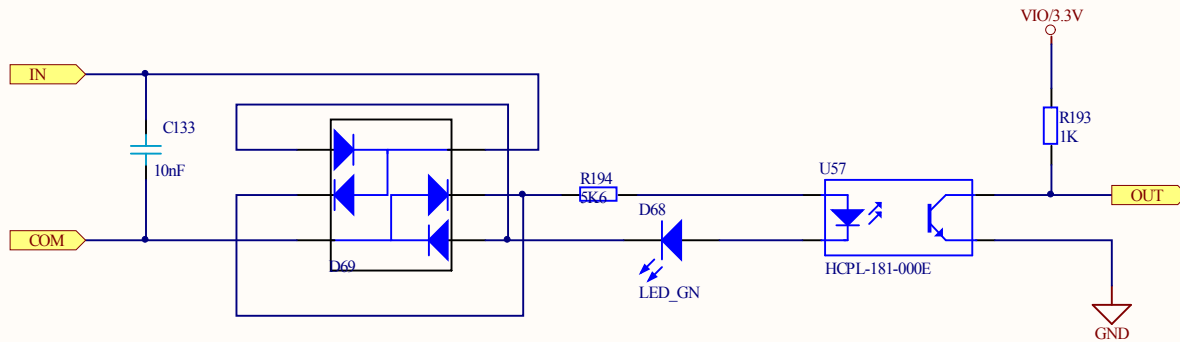
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.1	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.1 of: 49	
File: P:\QUA_2475\Tester\HMSCHDIGI_IN_FE.SchDoc			




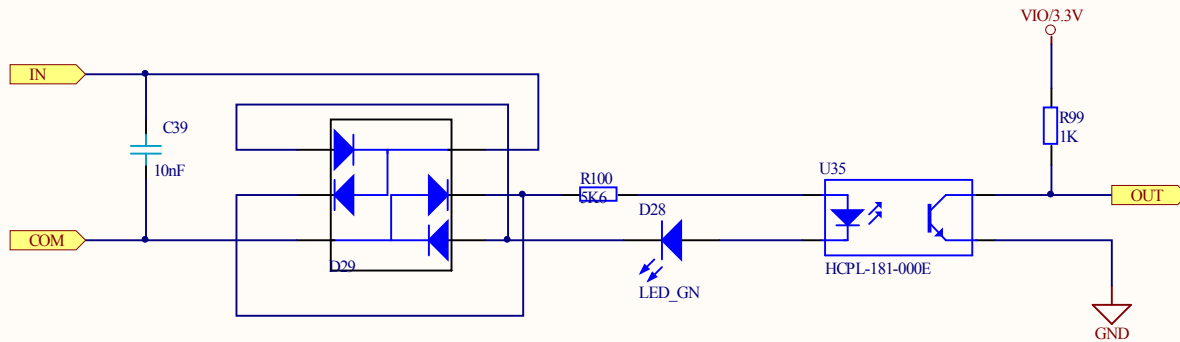
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.10	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.10 of 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




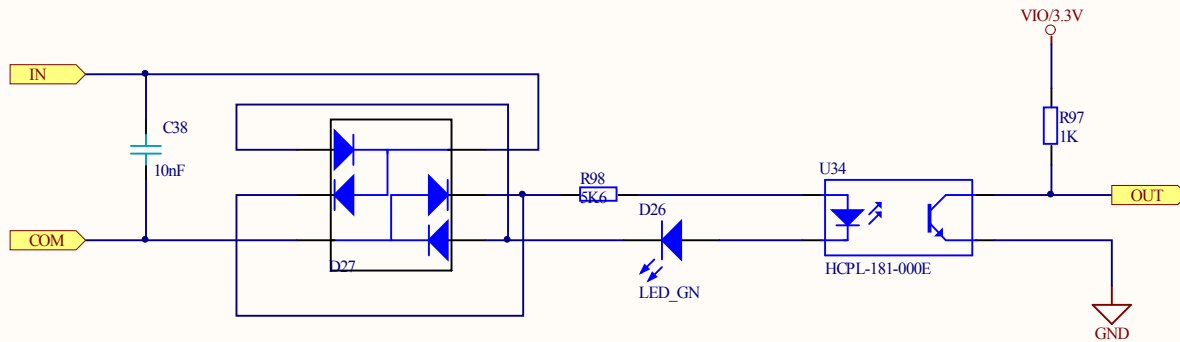
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.11	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.1 of 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




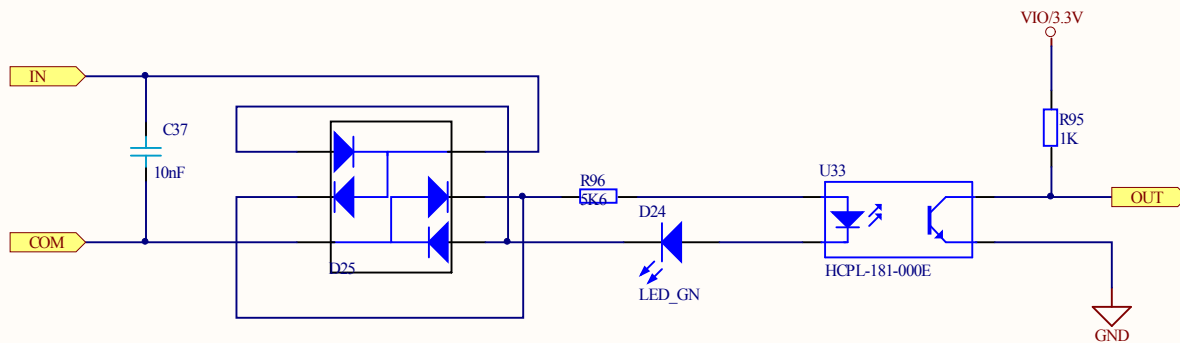
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.12	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.12 of 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




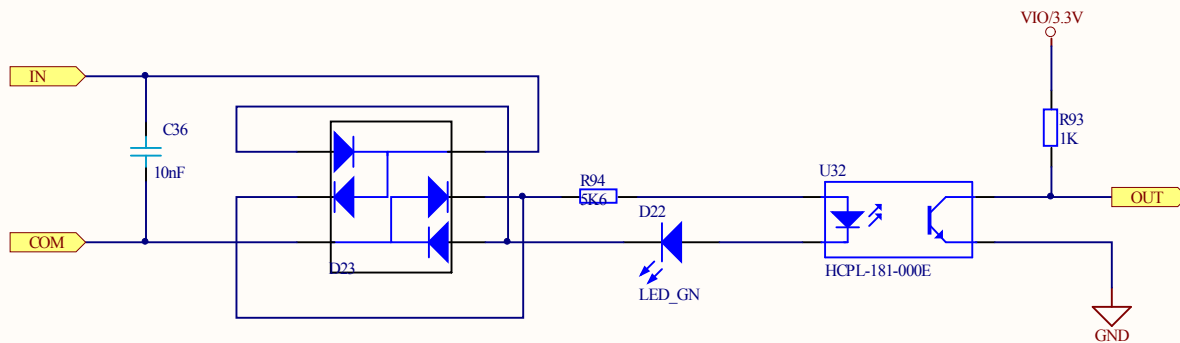
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.2	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.2 of : 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




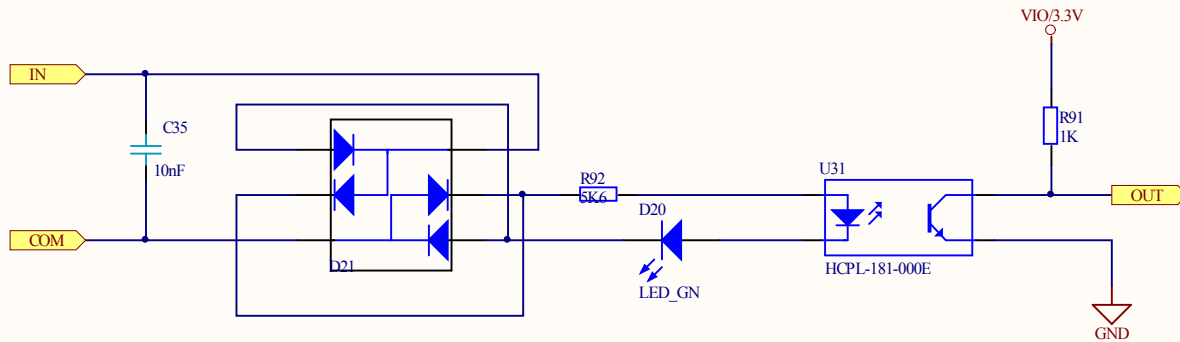
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.3	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.3 of : 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




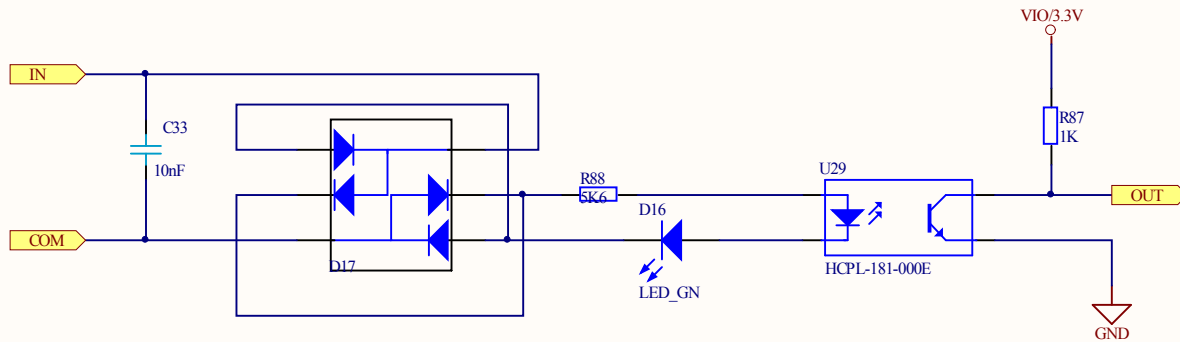
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.4	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.4 of: 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




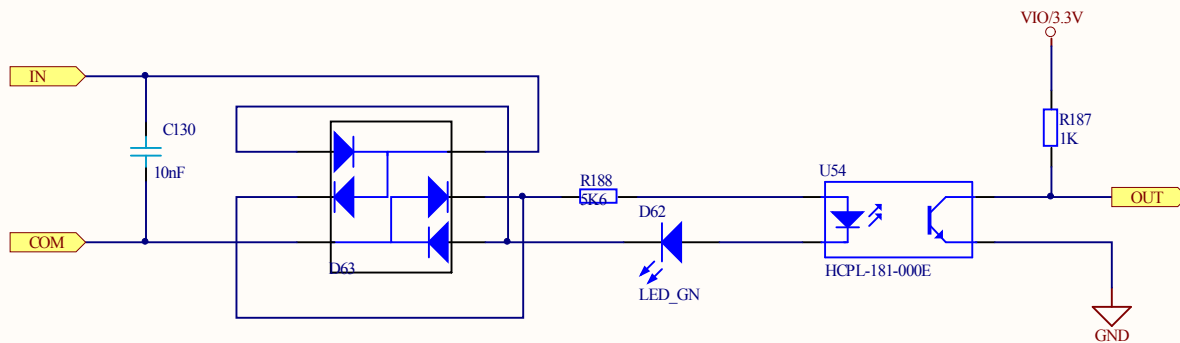
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.5	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.5 of : 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




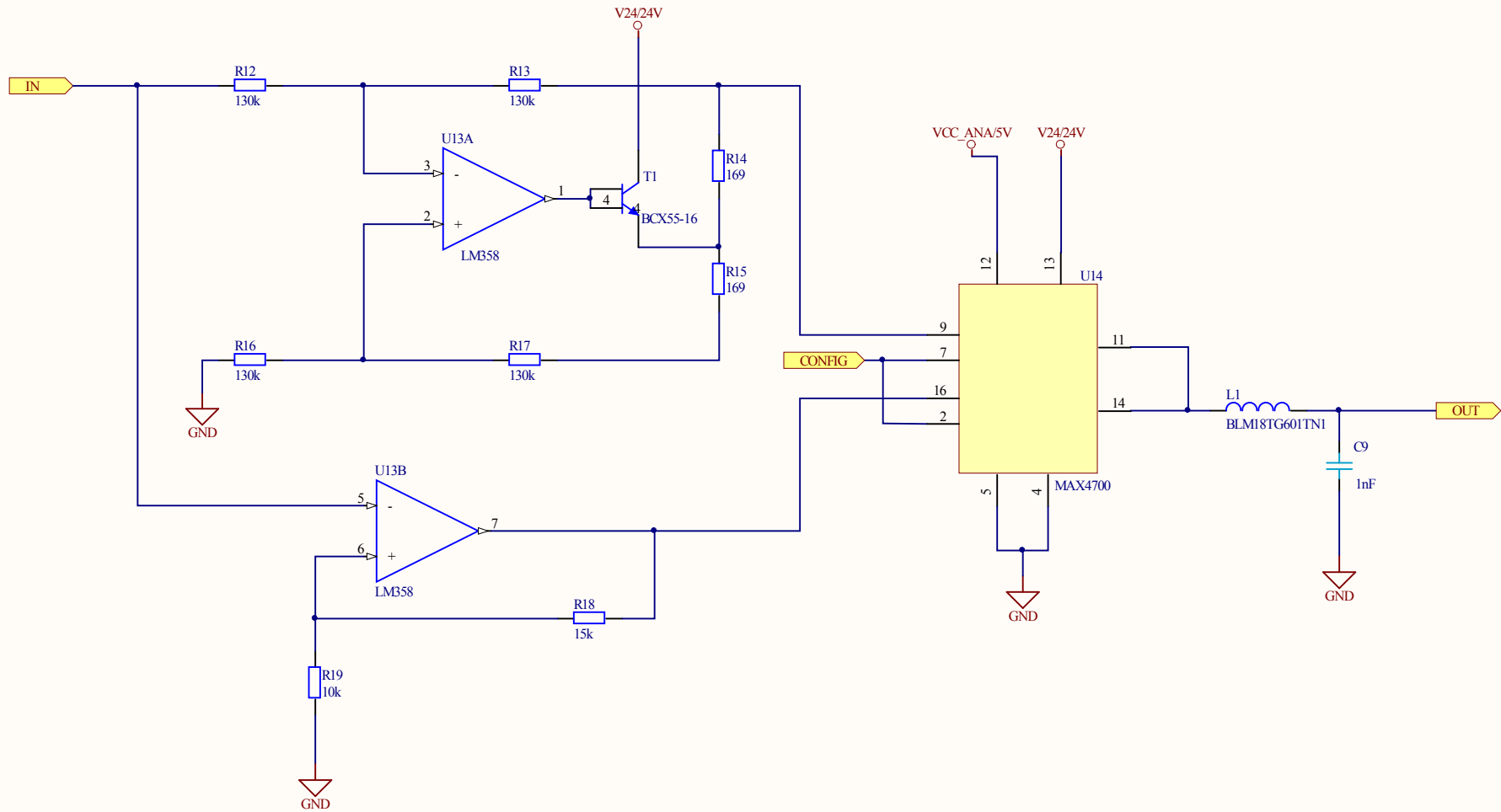
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.6	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.6 of: 49	
File: P:\QUA_2475\Tester\HMSCHDIGI_IN_FE.SchDoc			




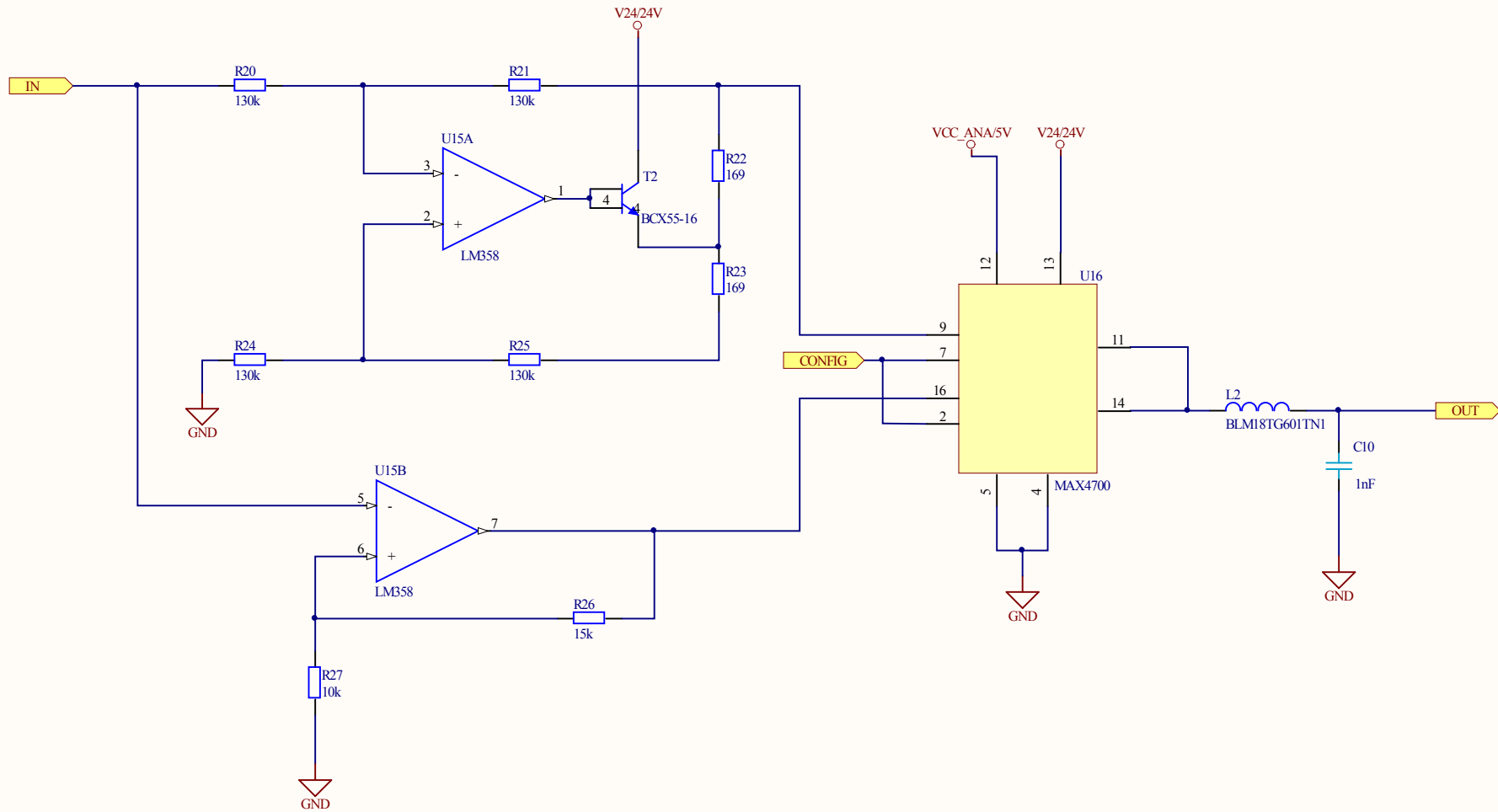
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.8	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.8 of : 49	
File: P:\QUA_2475\Tester\HMSCHDIGI_IN_FE.SchDoc			




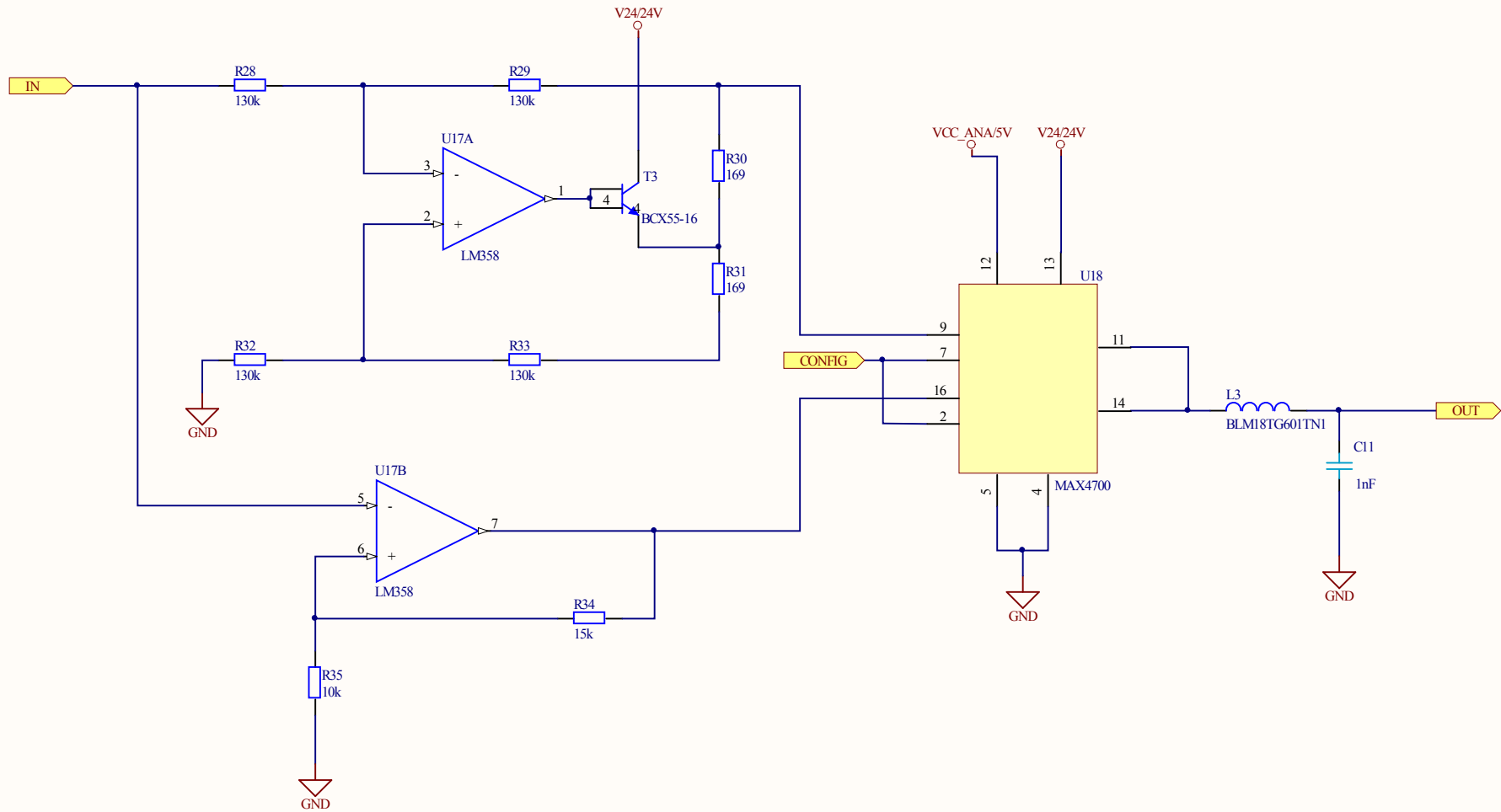
 Micro-Key Embedded Technology Experts		Title: Digital In FE	
Drawn: MM	Number: 018.9	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 18.9 of : 49	
File: P:\QUA_2475\Tester\HWSCHDIGI_IN_FE.SchDoc			




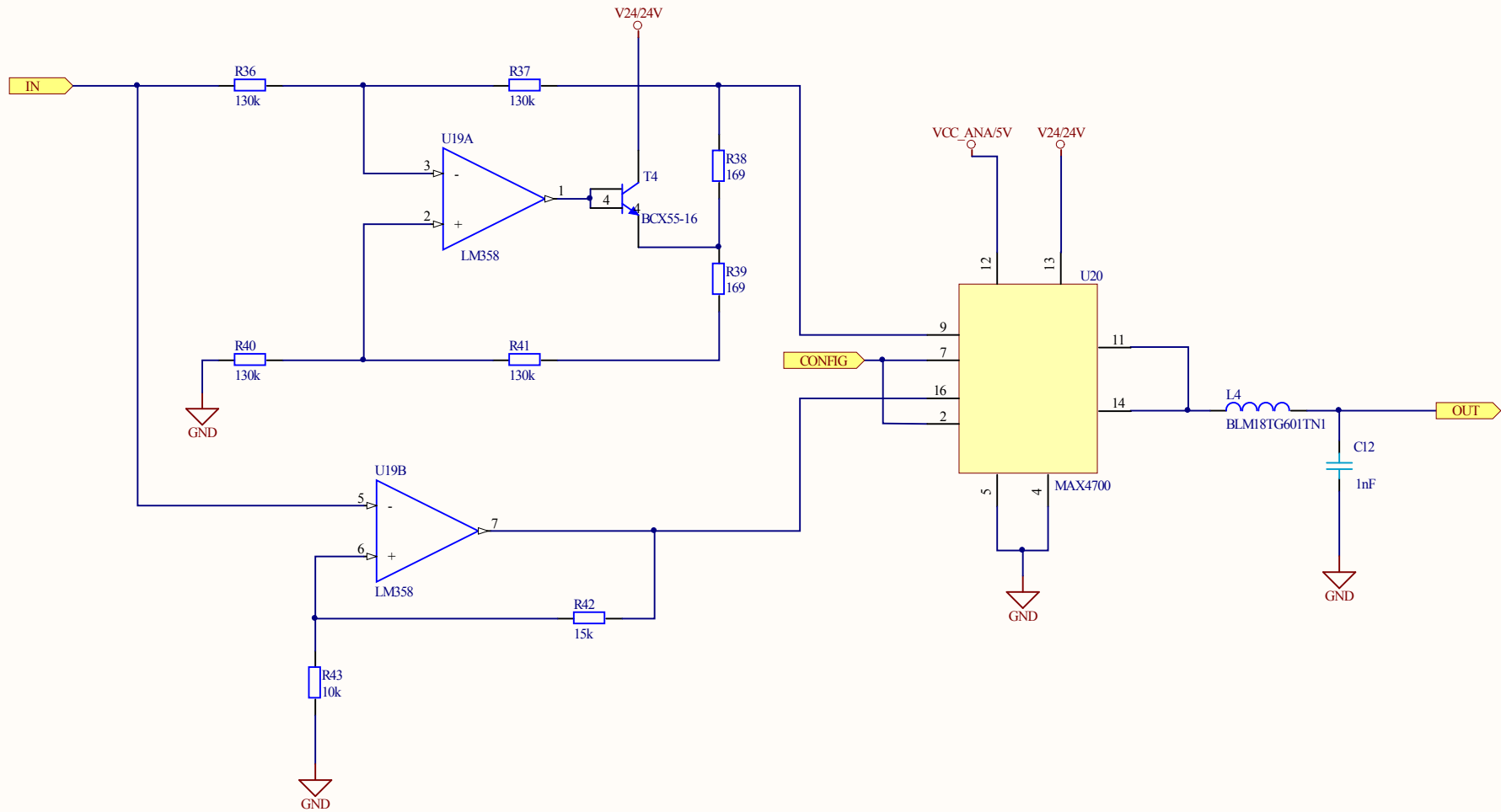
 Micro-Key Embedded Technology Experts		Title: Analogue Out BE	
Drawn: *	Number: 019.1	Rev: *	
Checked: *	Date: 9-1-2009	Sheet: 19.1 of: 49	
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			




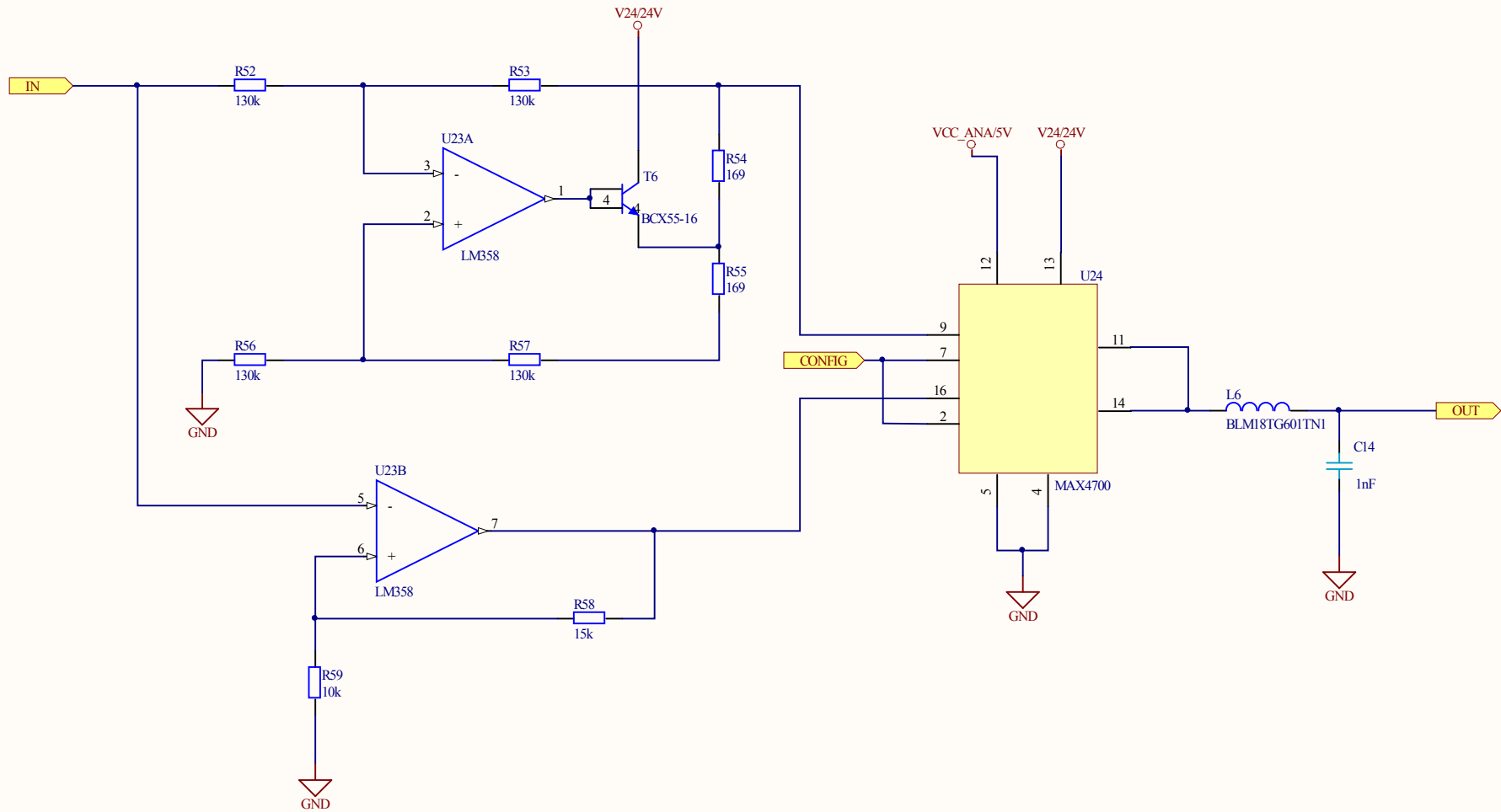
 Micro-Key Embedded Technology Experts		Title: Analogue Out BE	
Drawn: *		Number: 019.2	Rev: *
Checked: *		Date: 9-1-2009	Sheet: 19.2 of: 49
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			




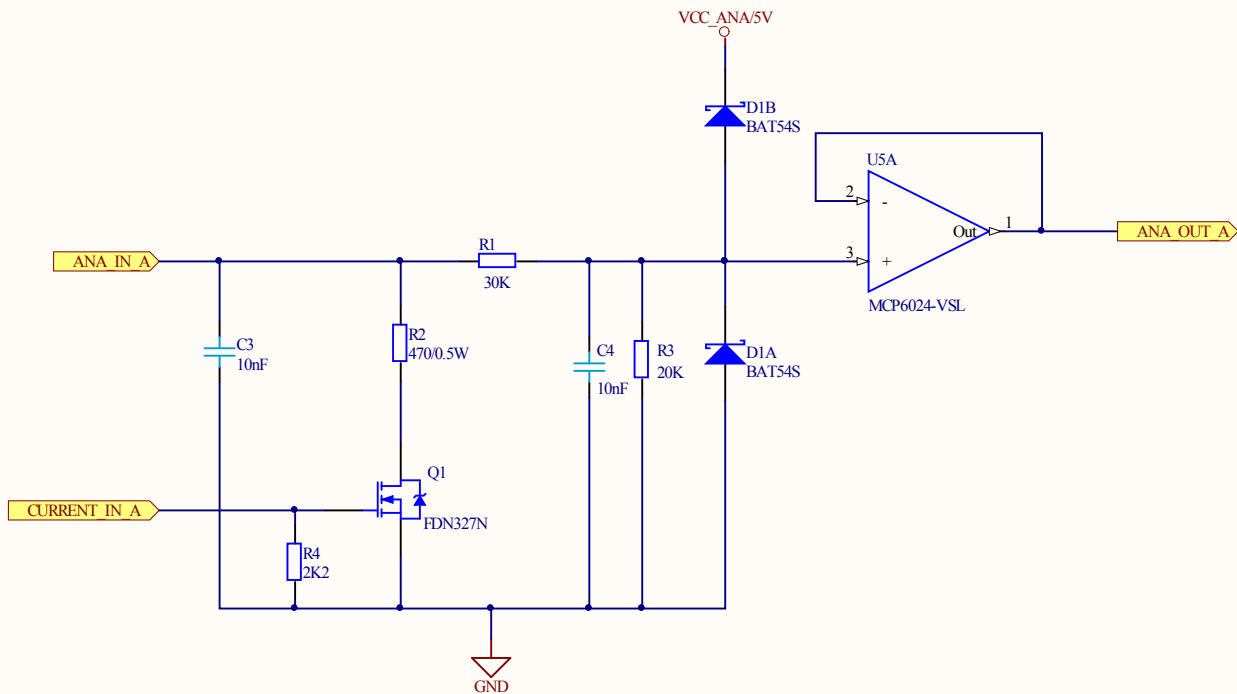
 Micro-Key Embedded Technology Experts		Title: Analogue Out BE	
Drawn: *	Number: 019.3	Rev: *	
Checked: *	Date: 9-1-2009	Sheet: 19.3 of: 49	
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			




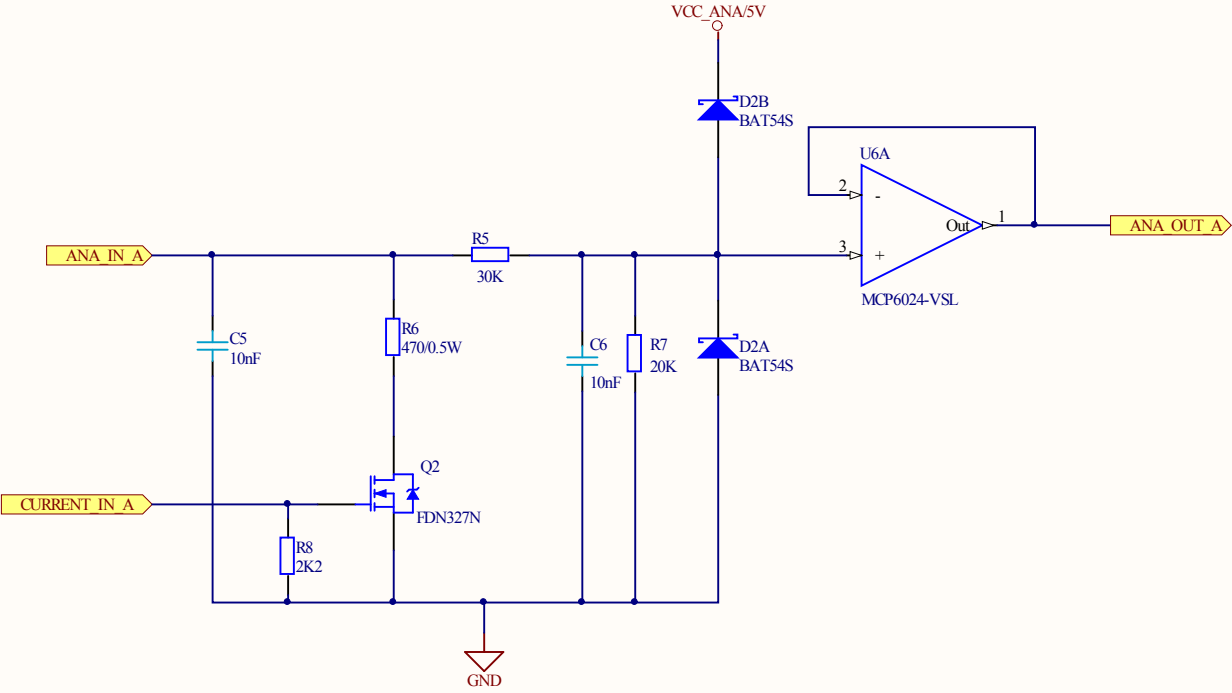
 Micro-Key Embedded Technology Experts		Title: Analogue Out BE	
Drawn: *	Number: 019.4	Rev: *	
Checked: *	Date: 9-1-2009	Sheet: 19.4 of: 49	
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			




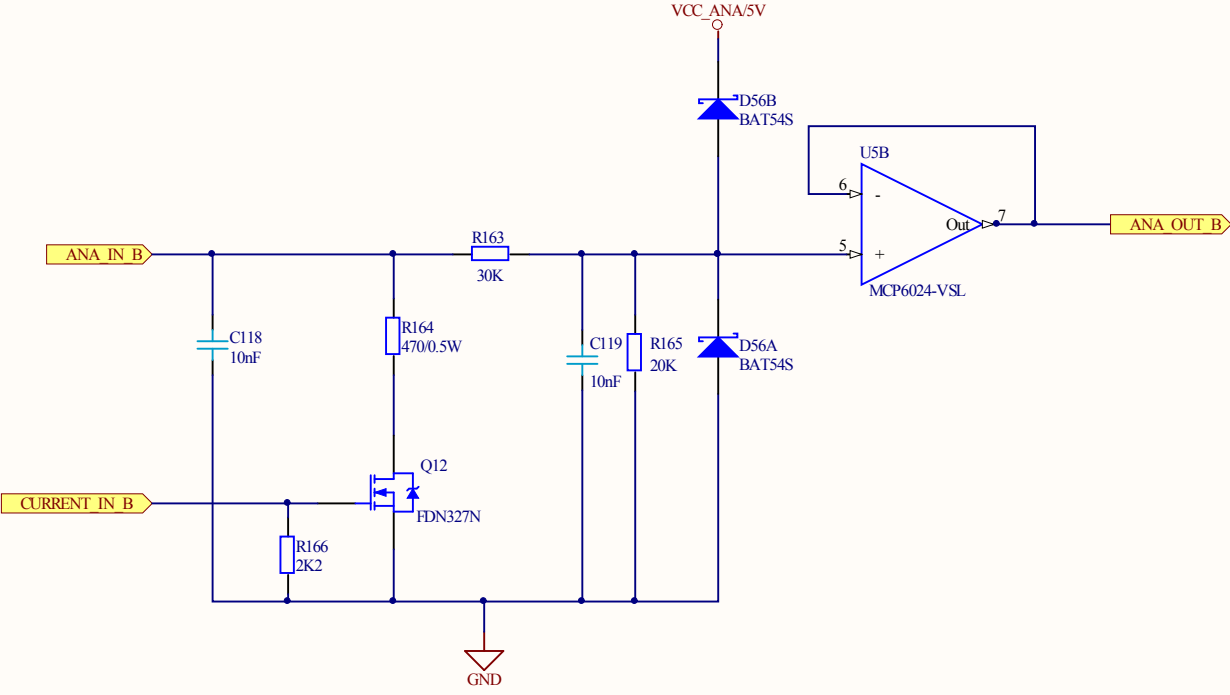
 Micro-Key Embedded Technology Experts		Title: Analogue Out BE	
Drawn: *	Number: 019.6	Rev: *	
Checked: *	Date: 9-1-2009	Sheet: 19.6 of : 49	
File: P:\QUA_2475\Tester\HMSCHANA_OUT_BE.SchDoc			




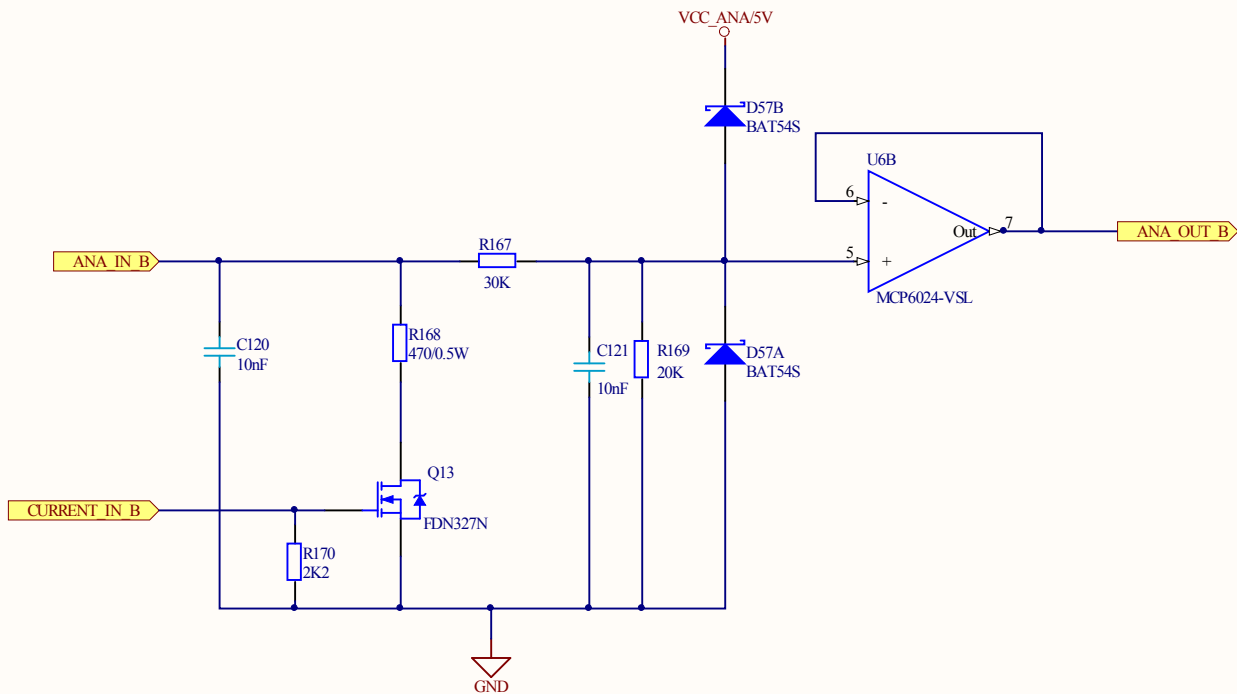
 Micro-Key Embedded Technology Experts		Title: Analogue In FE_A	
Drawn: MM	Number: 020.1	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 20.1 of: 49	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE.SchDoc			




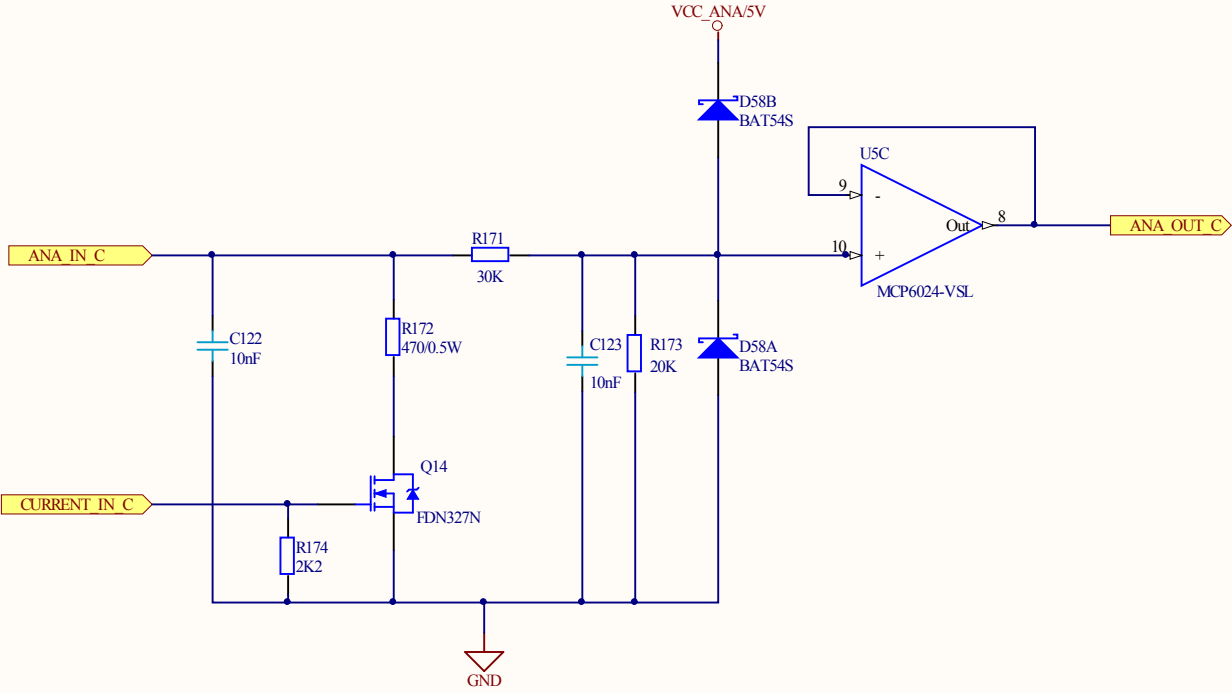
 Micro-Key Embedded Technology Experts		Title: Analogue In FE_A	
Drawn: MM	Number: 020.2	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 20.2 of : 49	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE.SchDoc			




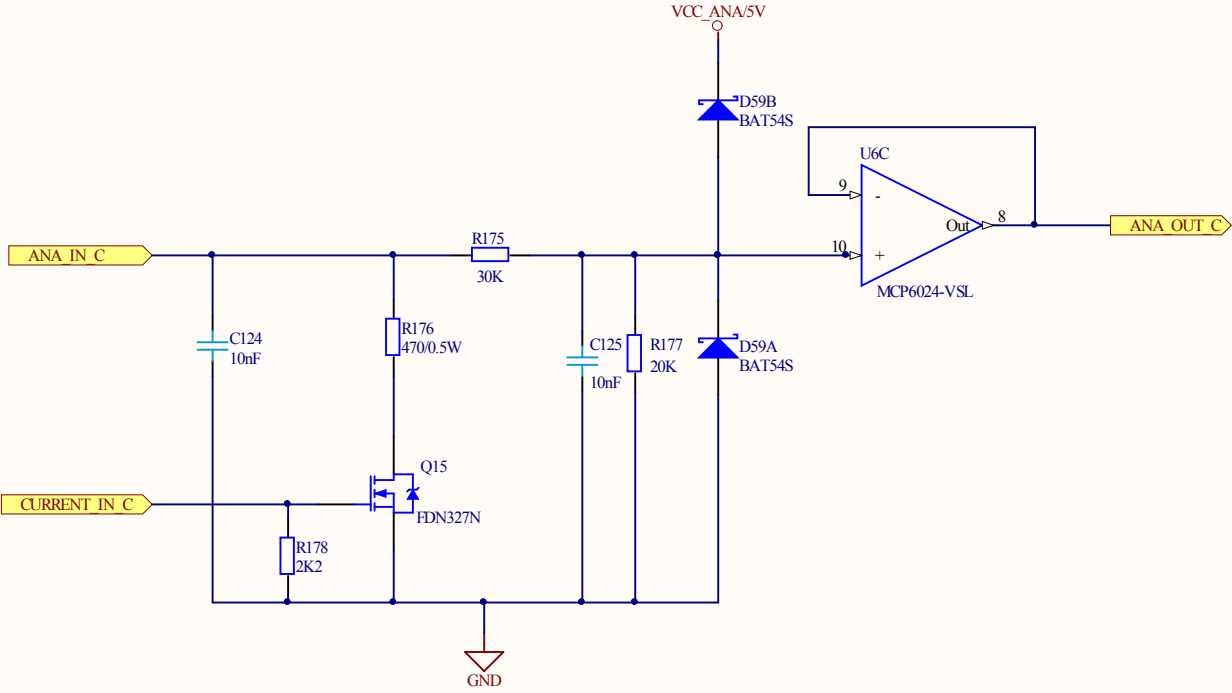
 Embedded Technology Experts	Title: Analogue In FE_B	
Drawn: MM	Number: 021.1	Rev: A
Checked: GSc	Date: 9-1-2009	Sheet: 21.1 of: 49
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_B.SchDoc		



 Micro-Key Embedded Technology Experts		Title: Analogue In FE_B	
Drawn: MM	Number: 021.2	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 21.2 of : 49	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_B.SchDoc			



 Micro-Key Embedded Technology Experts		Title: Analogue In FE_C	
Drawn: MM	Number: 022.1	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 22.1 of: 49	
File: P:\QUA_2475\Tester\HWSCHANA_IN_FE_C.SchDoc			



Title:
Analogue In FE_C

Drawn: MM

Number: 022.2

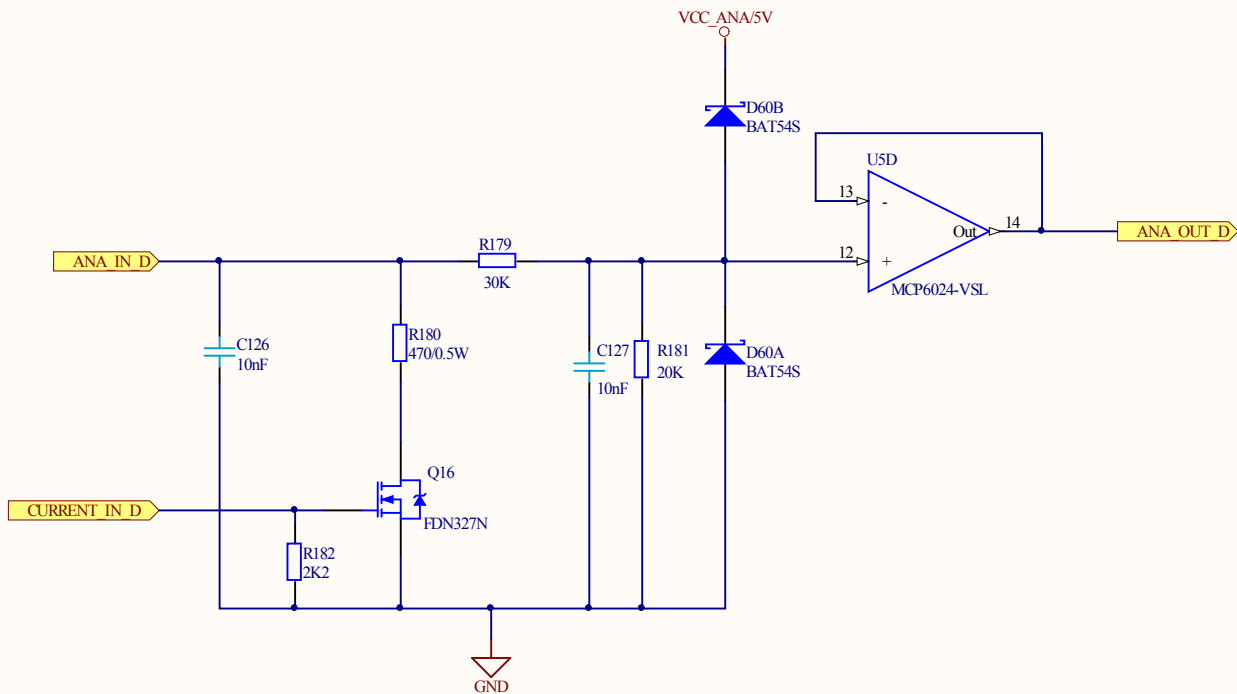
Rev: A


Checked: GSc

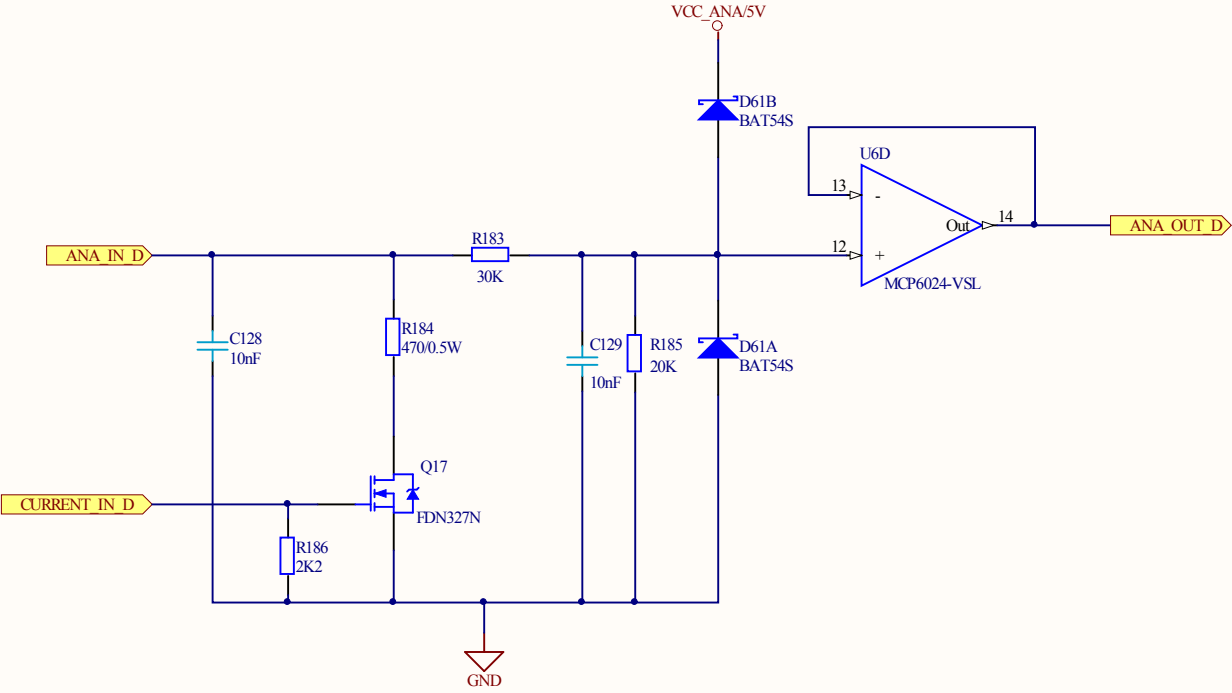
Date: 9-1-2009


Sheet: 22.2 of: 49

File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_C.SchDoc



 Micro-Key Embedded Technology Experts		Title: Analogue In FE_D	
Drawn: MM	Number: 023.1	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 23.1 of : 49	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_D.SchDoc			



 Micro-Key Embedded Technology Experts		Title: Analogue In FE_D	
Drawn: MM	Number: 023.2	Rev: A	
Checked: GSc	Date: 9-1-2009	Sheet: 23.2 of : 49	
File: P:\QUA_2475\Tester\HWSCH\ANA_IN_FE_D.SchDoc			